

## Application Information

Application number:: Unassigned  
Filing Date:: Herewith  
Application Type:: Regular  
Subject Matter:: Utility  
Suggested classification::  
Suggested Group Art Unit::  
CD-ROM or CD-R??::  
Number of CD disks::  
Number of copies of CDs::  
Sequence Submission::  
Computer Readable Form (CRF)?::  
Number of copies of CRF::  
Title:: EMBEDDED PROCESSOR WITH DUAL-PORT  
SRAM FOR PROGRAMMABLE LOGIC  
015114-053300US  
Attorney Docket Number::  
Request for Early Publication:: No  
Request for Non-Publication:: Yes  
Suggested Drawing Figure::  
Total Drawing Sheets:: 21  
Small Entity?:: No  
Latin name::  
Variety denomination name::  
Petition included?:: No  
Petition Type::  
Licensed US Govt. Agency::  
Contract or Grant Numbers One::  
Secrecy Order in Patent Appl.?:: No

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**Representative Information**

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**Domestic Priority Information**

Application:: Continuity Type:: Parent Application:: Parent Filing Date::

This Application is:: Non-Provisional of 60/211,094 June 12, 2000

**Foreign Priority Information**

Country:: Application number:: Filing Date::

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